

transistor are connected in common and have the same potential, and the differential circuit generates an internal signal at the first drain in accordance with a current flowing through the first and second transistors;

a constant current source connected to the first source of the first transistor; and

a current regulating circuit connected to the second source of the second transistor and connected in parallel to the constant current source, wherein the current regulating circuit increases and decreases an amount of the current flowing through the differential circuit in response to the internal signal wherein the internal signal is directly provided to the current regulating circuit.

Please insert the following new claim:

Sub E17
-- 21. (New) A semiconductor integrated circuit fetching a data signal in response to rising and falling edges of a data strobe signal, comprising:

a data strobe signal input circuit which receives the data strobe signal, wherein the data strobe signal input circuit includes,

a differential circuit having a first transistor and a second transistor to generate a differential output signal having a logic level, a first gate of the first transistor receiving the data strobe signal, a second gate of the second transistor receiving a reference signal, and sources of the first and second transistor being connected in common and having the same potential,

a current mirror circuit supplying a current to the differential circuit,

a constant current source coupled to the sources of the first and second transistors, and